

Serial No. 09/750,051

IN THE SPECIFICATION:

The specification as amended below with replacement paragraphs shows added text with underlining and deleted text with ~~strike through~~.

Please REPLACE the paragraph beginning at page 13, line 26, with the following paragraph:

FIG. 32 is a system block diagram for explaining the structure of an important part within the main body 101 of the computer system 100. In FIG. 32; the main body 101 generally includes a CPU 201, a memory part 202 made of RAM, ROM or the like, a disk drive 203 for the disk 110, and a hard disk drive (HDD) 204 which are connected via a bus 200. The display 102, the keyboard 103, the mouse 104 and the like may be connected to the CPU 201 via the bus 200 as shown in FIG. 3-2 or, connected directly to the CPU 201. It is also possible to connect the display 102 to the CPU 201 via a known graphic interface (not shown) which carries out an input/output image data processing.

Please REPLACE the paragraph beginning at page 16, line 15, with the following paragraph:

In the second embodiment, the input circuit information which is input in the step 1 is, for example, information amounting to one net shown in FIG. 5. The input circuit information shown in FIG. 5 includes a driver with a resistance $R_O = 20 \text{ Ohms } (\Omega)$, a wiring having a wiring length $L=20 \text{ mm}$ and an impedance $Z_O = 60 \text{ Ohms}$, a resistor having a resistance $R_s = 20 \text{ } \underline{10} \text{ Ohms}$, a wiring having a wiring length $L = 20 \text{ } \underline{150} \text{ mm}$ and an impedance $Z_O = 60 \text{ Ohms}$, and a receiver. In this case, a clock frequency of the net, a delay between pins, a signal propagation speed on a pattern, a layer structure, a pattern width and the like may be input in addition to the input circuit information shown in FIG. 5.